5	earcn	Notes

Application/Control No.	Applicant(s)/Patent under Reexamination
10/730,708	NUGENT, ALEX
Examiner	Art Unit
Peter Coughlan	2129

	SEAR	CHED	
Class	Subclass	Date	Examiner
706	1	6/24/2006	PDC
706	15	6/24/2006	PDC
706	45	6/24/2006	PDC
700	1	6/24/2006	PDC
700	90	6/24/2006	PDC
307	201	6/24/2006	PDC

INT	INTERFERENCE SEARCHED		
Class	Subclass	Date	Examiner
	<u> </u>		

EGY)		SEARCH NO (INCLUDING SEARCH	
EXMR	DATE		
006 PDC	6/24/2006	East—Alex Nugent, weight, nanopaticles, neural network, dielectric, strenghtening, increasing, self assembled, bezryadin	
006 PDC	6/24/2006	Eaat-II-solution, chip, alignment, transfer signals, threshold, output data, presynaptic, integrator, resistor, diode, sum,	
006 PDC	6/24/2006	East–IIIresistor to ground, time interval, memistor	
006 PDC	6/24/2006	IEEEAlex nugent, nanoparticles, neural network, dielectric solution, increasing, increasing, threshold, resistor, summation	
006 PDC	6/24/2006	IEEEIItime interval, memistor	
006 PDC	6/24/2006	Inventor Alex Nugent	
006 PDC	6/24/2006	307.201 with integrator, threshold	